

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Koichi MIZUGAKI et al.

Application No.: New US Application

Filed: February 4, 2004

Docket No.: 118065

For: SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. The present application was filed or entered the U.S. National Stage of the PCT after June 30, 2003. In accordance with the June 11, 2003, Notice waiving the requirements of 37 C.F.R. §1.98(a)(2)(i), copies of any U.S. patents and patent application publications are not attached.
- 3. English-language Abstracts of the non-English language references are attached hereto.
- 4. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([\[http://www.jpo.go.jp\]](http://www.jpo.go.jp)), and is attached, but has not been reviewed for accuracy. See Reference 2.
- 5. U.S. Patent No. 6,597,615 is from the same patent family as JP A 2002-74945.

Respectfully submitted,



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Date: February 4, 2004